

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No..... 10/625,068  
 Filing Date..... July 22, 2003  
 Inventor..... Arup Bhattacharyya  
 Assignee ..... Micron Technology, Inc.  
 Group Art Unit..... 2824  
 Examiner..... Christian D. Wilson  
 Attorney Docket No..... MI22-2362  
 Title: Methods of Making Semiconductor-on-Insulator Thin Film Transistor  
 Constructions

Commissioner for Patents  
 P.O. Box 1450  
 Alexandria, VA 22313-1450

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**NUMBER OF PAGES IN FACSIMILE: 10**

**FEE DEFICIENCY**

Although it is believed that no fees are due, the Commissioner is hereby authorized to charge any fees under 37 C.F.R. 1.16 and 1.17 which may be required by this paper to Deposit Account No. 23-0925.

Dated: 1/27/05

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Application Serial No. .... 10/625,068  
Confirmation No. .... 8254  
Filing Date ..... July 22, 2003  
Inventor..... Arup Bhattacharyya  
Assignee..... Micron Technology, Inc.  
Group Art Unit..... 2824  
Examiner ..... Christian D. Wilson  
Attorney's Docket No. .... MI22-2362  
Customer No. .... 021567  
Title: Methods of Making Semiconductor-on-Insulator Thin Film Transistor Constructions

**RESPONSE TO NOVEMBER 29, 2004 OFFICE ACTION**

To: Commissioner for Patents  
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**AMENDMENTS**